Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	(("6088783") or ("4760518")).PN.	USPAT	OR	OFF	2005/04/04 13:34
L2	340	712/11.CCLS.	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:35
L3	152	712/37.CCLS.	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:35
L4	173	712/38.CCLS.	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:35
L5	523	710/316.CCLS.	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:35
L6	214	710/317.CCLS.	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:36
L7	1337	2 OR 3 OR 4 OR 5 OR 6	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:36
L8	1242	(PLURAL$3 OR SECOND$3) NEAR3 ALU	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:36
L9	1110	REGISTER$1 AND 8	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:36
L10	164488	INPUT/OUTPUT	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:37
L11	434	9 AND 10	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:37
L12	538484	MICROPROCESSOR OR MICROCOMPUTER OR CHIP	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:37
L13	367	11 AND 12	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:38
L14	287	SWITCH$3 AND 13	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:38
L15	899882	PROGRAM$3 OR SOFTWARE OR INSTRUCTION$1	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:38
L16	286	14 AND 15	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:39
L17	87048	(MATRIX OR GRID OR CROSSBAR OR ARRAY) NEAR3 (INTERCONNECT$3 OR CONNECT$3 OR SWITCH)	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:40
L18	67	16 AND 17	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:42
L19	144	RECONFIGUR$3 NEAR3 MICROPROCESSOR	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:42
L20	0	9 AND 19	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:46
L21	87	SLIC$3 NEAR3 ALU	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:47

L22	28	9 AND 21	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:47
L23	1255	(MULTIPLE OR SECOND$3 OR PLURAL$3) NEAR3 (ARITHMETIC ADJ LOGIC)	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:48
L24	103	RECONFIGUR$3 AND 23	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:48
L25	100	REGISTER$1 AND 24	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:49
L26	1038845	SWITCH$3 OR (INTERCONNECT$3 NEAR3 (GRID OR ARRAY OR MATRIX))	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:50
L27	72	25 AND 26	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:50
L28	1327	SELECT$3 NEAR3 ALU	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:51
L29	19	MULTIPROCESSOR WITH ALU	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:51
L30	208	ALU AND 7	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:51
L31	198	ALU WITH PE	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:51
L32	557770	ARRAY OR MATIX OR GRID	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:51
L33	140	31 AND 32	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:51
L34	56	COLUMN NEAR3 ALU	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:52
L35	47235	(SEPARATE OR INDIVIDUAL OR DIFFERENT) NEAR3 (CONDUCTOR$1 OR BUS$2)	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:53
L36	1376	32 WITH 35	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:53
L37	389	INPUT NEAR3 OUTPUT NEAR3 35	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:54
L38	1	37 WITH ALU	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:55
L39	68	CROSSBAR NEAR3 GRID$1	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:55
L40	141	CROSSBAR WITH GRID$1	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:55
L41	3161	GRID NEAR3 CONNECTION$1	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:56
L42	723139	MEMORY OR (CROSSBAR OR CROSSPOINT)	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:56

L43	190444	REGISTER$1 AND 42	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:57
L44	29693	ARITHMETIC AND 43	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:58
L45	0	CROSSBARY ADJ SWITCH$3	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:58
L46	242661	X ADJ Y	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:58
L47	3445	CROSSBAR ADJ SWITCH$3	US-PGPUB; USPAT	OR	OFF	2005/04/04 13:58
L48	24	46 WITH 47	US-PGPUB; USPAT	OR	OFF	2005/04/04 14:00
L49	371	(INDIVIDUAL$3 OR SEPARATE$2 OR INDEPENDENT$2) NEAR3 BUS$2 NEAR3 INPUT$1 NEAR3 OUTPUT$1	US-PGPUB; USPAT	OR	OFF	2005/04/04 14:01
L50	94	17 AND 49	US-PGPUB; USPAT	OR	OFF	2005/04/04 14:02
L51	2	49 WITH ((ARITHMETIC ADJ LOGIC) OR ALU)	US-PGPUB; USPAT	OR	OFF	2005/04/04 14:02
L52	0	50 AND 51	US-PGPUB; USPAT	OR	OFF	2005/04/04 14:02
L53	2	50 AND 7	US-PGPUB; USPAT	OR	OFF	2005/04/04 14:03